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Application Number:	10672960			
International Application Number:				
Confirmation Number:	9440			
Title of Invention:	SYSTEM AND METHOD FOR LIMITED FANOUT DAISY CHAINING OF CACHE INVALIDATION REQUESTS IN A SHARED-MEMORY MULTIPROCESSOR SYSTEM			
First Named Inventor/Applicant Name:	Kourosh Gharachorloo			
Correspondence Address:	HAWLETT-PACKARD COMPANY Intellectual Property Administration P. O. Box 272400 - Fort Collins CO 80527-2400 US 2815189645 -			
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1 Issue Fee Payment (PTO-85B)	10-672960.pdf	252170	no	3	
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New International Application Filed with the USPTO as a Receiving Office

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